

850nm 10Gb/s Multi-Mode VCSEL Chip

(Preliminary)

Features:

- 850nm multi-mode emission
- Low threshold and operation current
- High reliability
- Low electrical parasitics
- Data rates from DC to 10 Gb/s
- Backside cathode and topside anode configuration

Applications:

- Fiber optical communication links
- Smart cables, HDMI, consumer applications



Bookham's high speed 850nm multi-mode VCSEL chip is designed to meet stringent specifications for high speed fiber optical communication links. The high performance, high reliability device is engineered with low electrical parasitics for data rates up to 10Gbps. The VCSEL operates in multiple transverse and single longitudinal modes and emits a circular symmetric beam with low divergence that can be efficiently coupled into 50/125 and 62.5/125µm multi-mode fibers. The VCSEL has the cathode contact on the bottom side and the anode contact on the top side of the chip.

www.oclaro.com



Electro – Optical Characteristics*

Davamastav	S. mala al	Conditions	Ratings			11-24	
Parameter	Symbol	Conditions	Min	Тур	Max	Unit	
Threshold current	I _{th}			0.8	1.2	mA	
Optical output power	Pout	lop = 6mA		2.5		mW	
Slope efficiency	η	I=I _{th} +1mA	0.3	0.5	0.7	mW/mA	
Operating voltage	Uop	lop = 6mA		1.9		٧	
Differential resistance	Rd	lop = 6mA		50	80	Ω	
Emission wavelength	λ	lop = 6mA, T=0°C - 70°C	830	850	860	nm	
Spectral bandwidth, RMS	Δλ	lop = 6mA			0.65	nm	
Beam divergence	Θ	I_{op} = 6mA, Full width 1/e ²		25	30	0	
Capacitance	С	lop = 6mA		0.35	0.5	рF	
Modulation bandwidth	f _{3dB}	lop = 6mA	8			GHz	
Rise time	tr	I _{op} = 6mA, ER=5dB, 20% - 80%		30	40	ps	
Fall time	† _f	l _{op} = 6mA, ER=5dB, 20% - 80%		40	45	ps	
Relative Intensity Noise	RIN(OMA)	l _{op} = 6mA, ER=5dB, 7.7GHz bandwidth			-128	dB/Hz	

Thermal Characteristics

Davamakar	Symala al		Unit		
Parameter Parameter	Symbol		Тур	Max	Unii
Wavelength tuning co-efficient	δΙ/δΤ		0.06		nm/K
Threshold current variation 0 to +70°C	Δl_{th}			1.0	mA
Slope efficiency variation 0 to +70°C	$\Delta\eta_{T}$		-0.35		%/K
Thermal impedance	Zth		2.0		K/mW

^{*}T=25°C unless otherwise noted

Absolute Maximum Ratings

Parameter	Rating	Unit
Optical output power	8	mW
Peak forward current	12	mA
VCSEL reverse voltage	5	٧
Operating temperature	0 to +70	°C
Storage temperature	-40 to +100	°C
Mounting temperature (max. 1h)	165	°C

2 www.oclaro.com

Data Sheet



Chip Dimensions

Parameter		Rating	Unit	
		Тур	Max	Unii
Die length	170	190	210	μm
Die width	170	190	210	μm
Die height	135	150	165	μm

3 www.oclaro.com



RoHS Compliance





Oclaro is fully committed to environment protection and sustainable development and has set in place a comprehensive program for removing polluting and hazardous substances from all of its products. The relevant evidence of RoHS compliance is held as part of our controlled documentation for each of our compliant products. RoHS compliance parts are available to order, please refer to the ordering information section for further details.

Ordering Information:

Data Rate **Product Code**

APA7501010000 10 Gb/s **Package**

850nm MM single VCSEL chip

Contact Information

Oclaro Inc. **Worldwide Headquarters** 2584 Junction Avenue San Jose CA 95134 USA

Tel: +1 408 919 2788 Fax: +1 408 904 5026

www.oclaro.com sales@oclaro.com

Important Notice

Performance figures, data and any illustrative material provided in this data sheet are typical and must be specifically confirmed in writing by Oclaro before they become applicable to any particular order or contract. In accordance with the Oclaro policy of continuous improvement specifications may change without notice. The publication of information in this data sheet does not imply freedom from patent or other protective rights of Oclaro or others. Further details are available from any Oclaro sales representative.

Safety Labels





APA7501010000 Rev 1, October 2009 ©Oclaro 2009. Oclaro the Oclaro, Inc. logo, and all other Oclaro, Inc product names and slogans are trademarks or registered trademarks of Oclaro, Inc. in the U.S.A. or other countries. Products described in this datasheet may be covered by one or more patents in the U.S.A. and abroad. Information in this datasheet is subject to change without notice.